

Schottky Rectifier, 2.1 A

10MQ100NPbF



SMA

Cathode ————— Anode

FEATURES

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The 10MQ100NPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

PRODUCT SUMMARY	
I _{F(AV)}	2.1 A
V _R	100 V

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
I _{F(AV)}	DC	2.1	A
V _{RRM}		100	V
I _{FSM}	t _p = 5 µs sine	120	A
V _F	1.5 Apk, T _J = 125 °C	0.68	V
T _J	Range	- 55 to 150	°C

VOLTAGE RATINGS			
PARAMETER	SYMBOL	10MQ100NPbF	UNITS
Maximum DC reverse voltage	V _R	100	V
Maximum working peak reverse voltage	V _{RWM}		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current See fig. 4	I _{F(AV)}	50 % duty cycle at T _L = 126 °C, rectangular waveform On PC board 9 mm ² island (0.013 mm thick copper pad area)		1.5	A	
Maximum peak one cycle non-repetitive surge current, T _J = 25 °C See fig. 6	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	120	A	
		10 ms sine or 6 ms rect. pulse		30		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 0.5 A, L = 8 mH		1.0	mJ	
Repetitive avalanche current	I _{AR}			0.5	A	

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ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop See fig. 1	V_{FM} (1)	1 A	$T_J = 25^\circ\text{C}$	0.78	V	
		1.5 A		0.85		
		1 A	$T_J = 125^\circ\text{C}$	0.63		
		1.5 A		0.68		
Maximum reverse leakage current See fig. 2	I_{RM} (1)	$T_J = 25^\circ\text{C}$	$V_R = \text{Rated } V_R$	0.1	mA	
		$T_J = 125^\circ\text{C}$		1		
Threshold voltage	$V_{F(TO)}$	$T_J = T_J$ maximum		0.52	V	
Forward slope resistance	r_t			78.4	$\text{m}\Omega$	
Typical junction capacitance	C_T	$V_R = 10 \text{ V}_{\text{DC}}$, $T_J = 25^\circ\text{C}$, test signal = 1 MHz		38	pF	
Typical series inductance	L_S	Measured lead to lead 5 mm from package body		2.0	nH	
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	$\text{V}/\mu\text{s}$	

Note

(1) Pulse width < 300 μs , duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J (1), T_{Stg}		- 55 to 150	$^\circ\text{C}$
Maximum thermal resistance, junction to ambient	R_{thJA}	DC operation	80	$^\circ\text{C/W}$
Approximate weight			0.07	g
			0.002	oz.
Marking device		Case style SMA (similar D-64)	V1J	

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

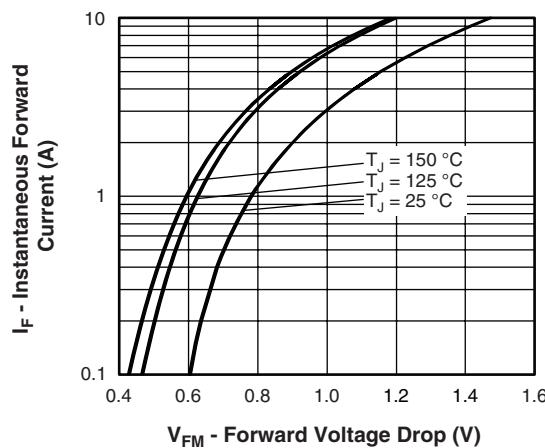


Fig. 1 - Maximum Forward Voltage Drop Characteristics

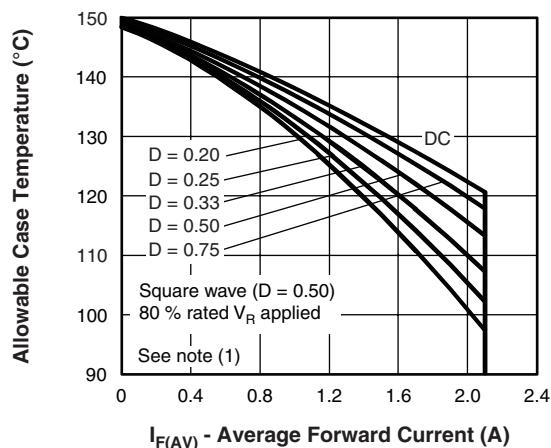


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

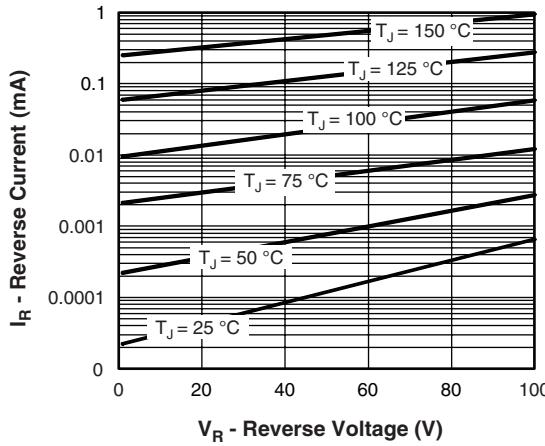


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

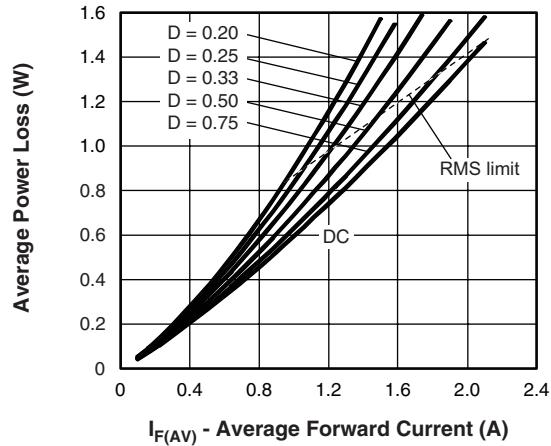


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

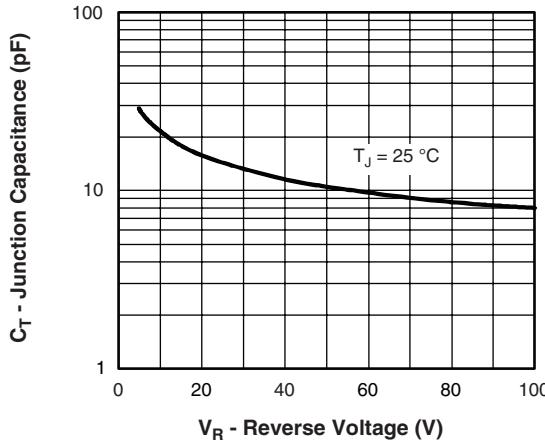


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

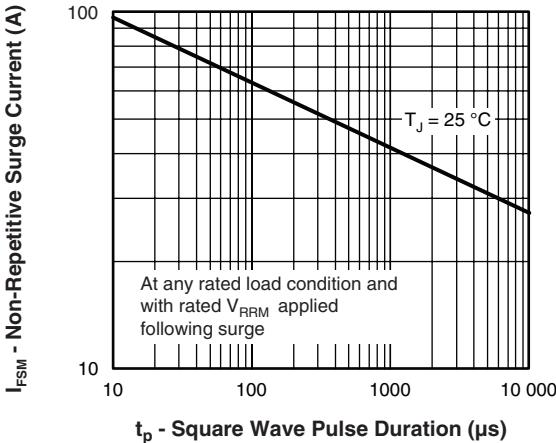


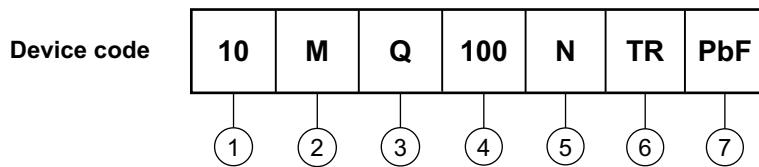
Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)})/D$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R

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ORDERING INFORMATION TABLE



- 1** - Current rating
- 2** - M = SMA
- 3** - Q = Schottky "Q" series
- 4** - Voltage rating (100 = 100 V)
- 5** - N = New SMA
- 6** - • None = Box (1000 pieces)
• TR = Tape and reel (7500 pieces)
- 7** - • None = Standard production
• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95018
Part marking information	http://www.vishay.com/doc?95029
Packaging information	http://www.vishay.com/doc?95034